

Applicants :	Reif et al.	Art Unit :	2811
Serial No. :	10/749,096	Examiner :	Owens, Douglas W.
Filed :	December 30, 2003		
Title :	MULT-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE HAVING AN ELECTRICAL SHIELDING PORTION		

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Please amend the above-identified application as follows:**

3- November - 2005  
Date of Deposit or Transmission  
Cara A. Murphy  
Signature  
Cara A. Murphy  
Typed or Printed Name of Person Signing Certificate